



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kevin J. Ryan Examiner: James Peikari
Serial No.: 09/434,082 Group Art Unit: 2186
Filed: November 05, 1999 Docket: 303.306US2
Title: PIPELINED PACKET-ORIENTED MEMORY SYSTEM HAVING A
UNIDIRECTIONAL COMMAND AND ADDRESS BUS AND A
BIDIRECTIONAL DATA BUS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement.

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The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

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By his Representatives,

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By 
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 14 day of December, 2004.

Name

Tina Kohaut

Signature

ZLF

INFORMATION DISCLOSURE STATEMENT BY APPLICANT <small>(Use as many sheets as necessary)</small>		Complete if Known <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%; padding: 2px;">Application Number</td> <td style="width: 50%; padding: 2px;">09/434,082</td> </tr> <tr> <td style="padding: 2px;">Filing Date</td> <td style="padding: 2px;">November 5, 1999</td> </tr> <tr> <td style="padding: 2px;">First Named Inventor</td> <td style="padding: 2px;">Ryan, Kevin</td> </tr> <tr> <td style="padding: 2px;">Group Art Unit</td> <td style="padding: 2px;">2186</td> </tr> <tr> <td style="padding: 2px;">Examiner Name</td> <td style="padding: 2px;">Peikari, James</td> </tr> </table>		Application Number	09/434,082	Filing Date	November 5, 1999	First Named Inventor	Ryan, Kevin	Group Art Unit	2186	Examiner Name	Peikari, James
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Examiner Name	Peikari, James												
Sheet 1 of 1		Attorney Docket No: 303.306US2											

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
	US-5,270,964	12/14/1993	Bechtolsheim, , et al.	05/19/1992
	US-5,598,545	01/28/1997	Childers, J. , et al.	03/25/1993
	US-5,625,780	04/29/1997	Hsieh, Wen-Jai , et al.	11/02/1994
	US-5,721,862	02/01/1998	Sartore, , et al.	
	US-5,903,509	05/11/1999	Ryan, K. , et al.	05/04/1998
	US-6,172,893	01/09/2001	Ryan, K.	01/05/1999

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		"400 Mb/s/pin SDRAM, 4M x 18 SDRAM, Pipelined, Eight Bank, 2.5V Operation", <u>SLDRAM Inc.</u> , Draft/Advance, SLD4M18DR400, 4 MEG x 18 SDRAM,(1998),pp. 1-69	
		"Draft Standard for A High-Speed Memory Interface (SyncLink)", <u>The Institute of Electrical and Electronics Engineers, Inc.</u> , Draft 0.99 IEEE P1596.7-199X,(1996),pp. 1-55	
		"New NEC Memory Technology Increases Performance by 20%", http://www.nb-pacifica.com/headline/newnecmemorytechnolo_1144.sh ,(1997),pp. 1-2	
		"Virtual Channel Memory", <u>NEC Electronics Inc.</u> , (1997),pp. 1-2	
		GILLINGHAM, P. , "SLDRAM Architectural and Functional Overview", <u>The SDRAM Consortium</u> , (Aug. 1997),pp. 1-14	

EXAMINER

DATE CONSIDERED